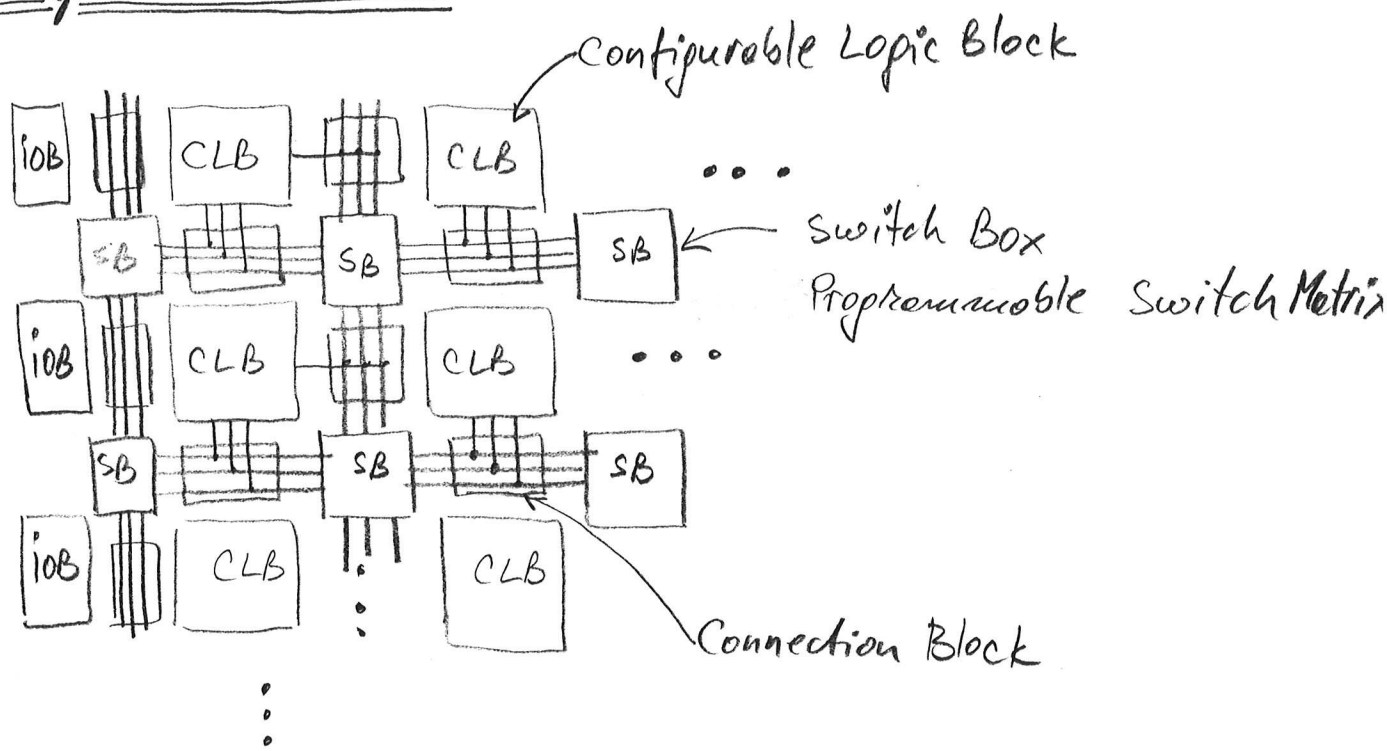
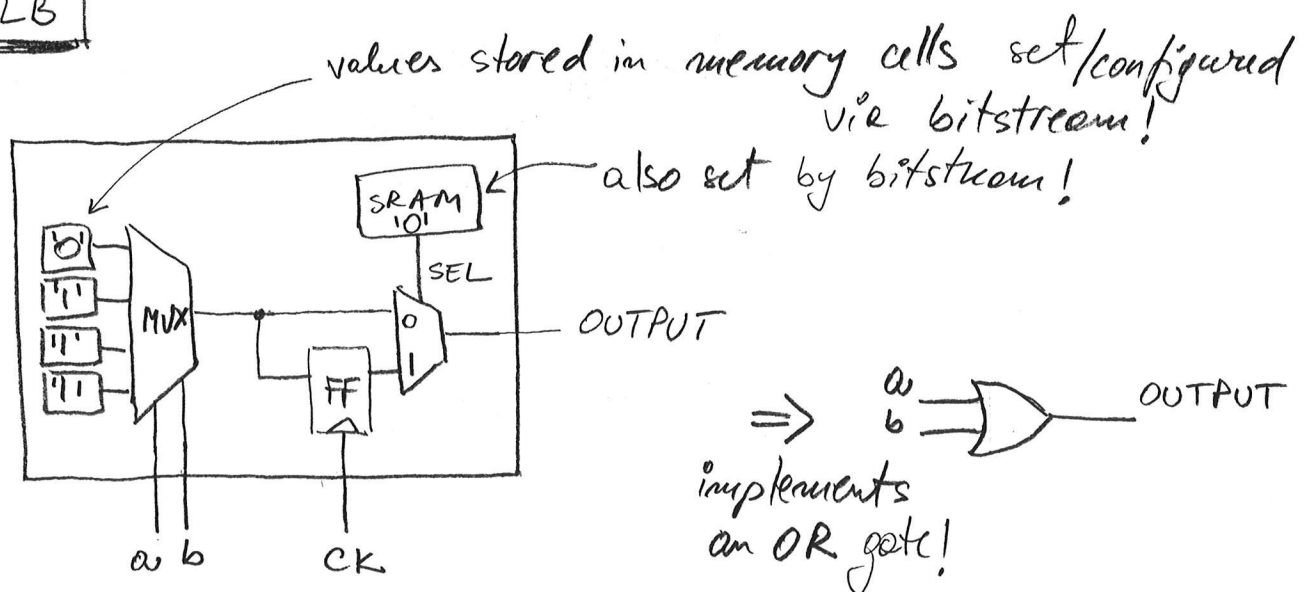


FPGAs

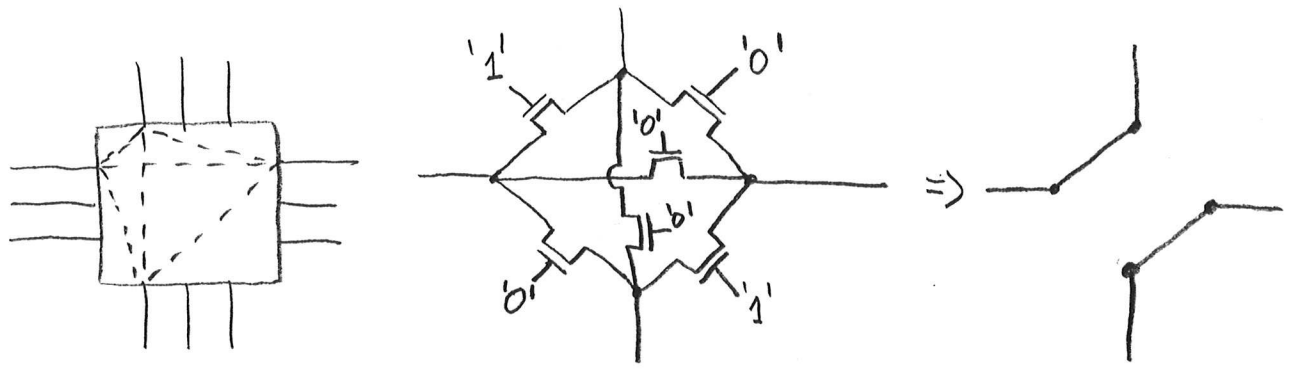
Simplest architecture



CLB



SB



Example

